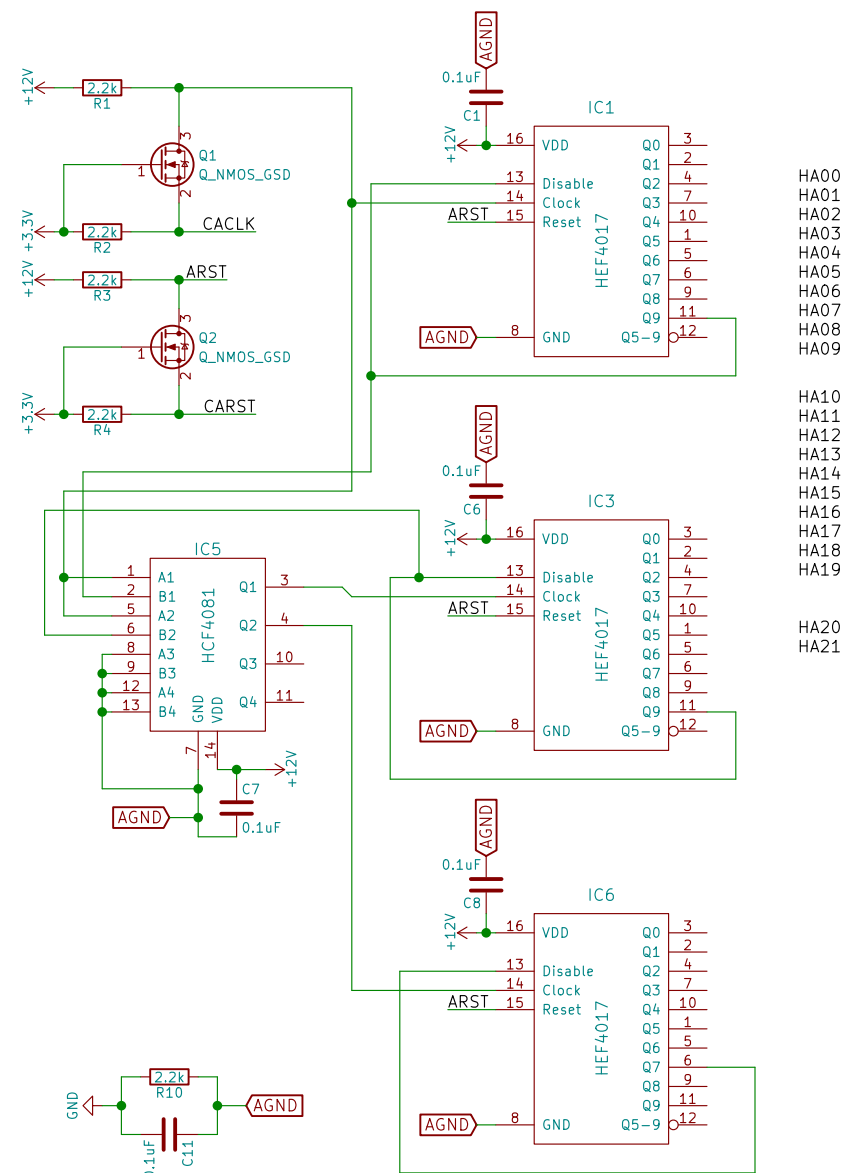
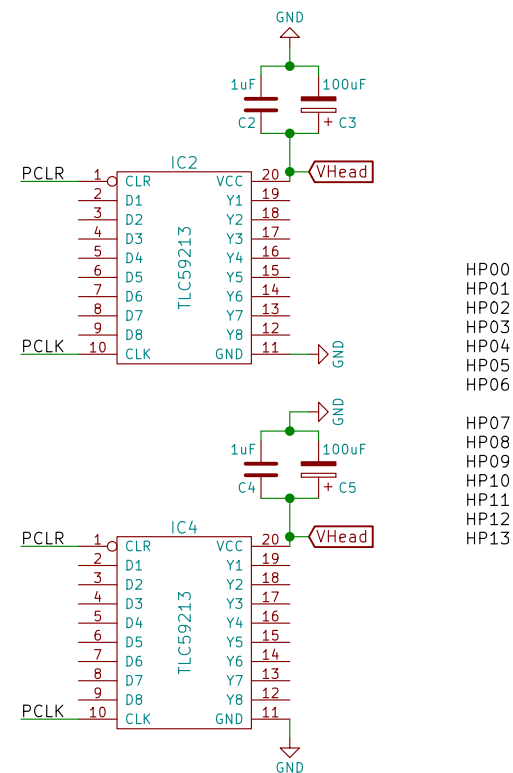


Address controller



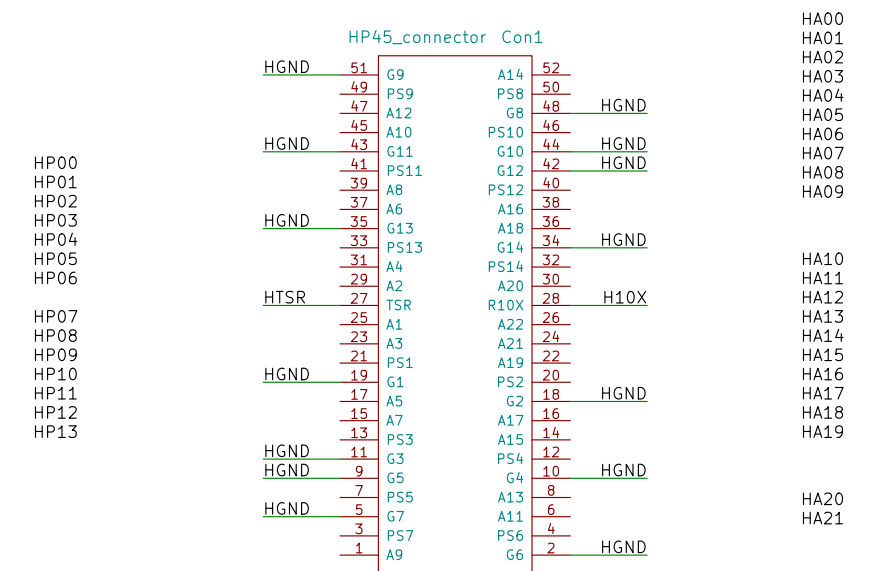
Exact Address layout determined in PCB phase

Primitive controller

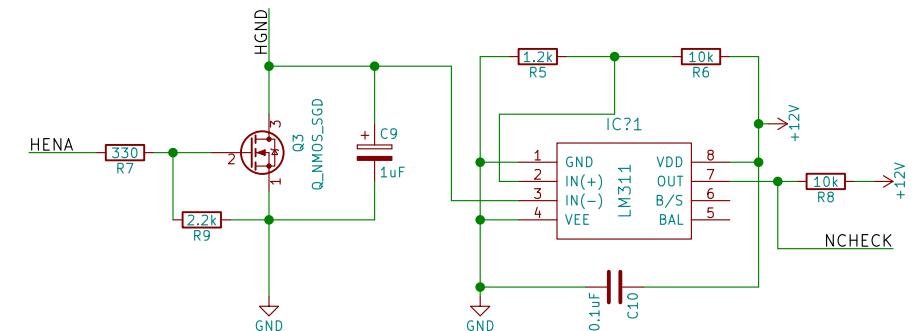


Exact primitive layout determined in PCB phase

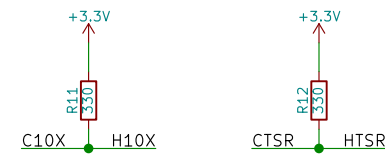
HP45 connector



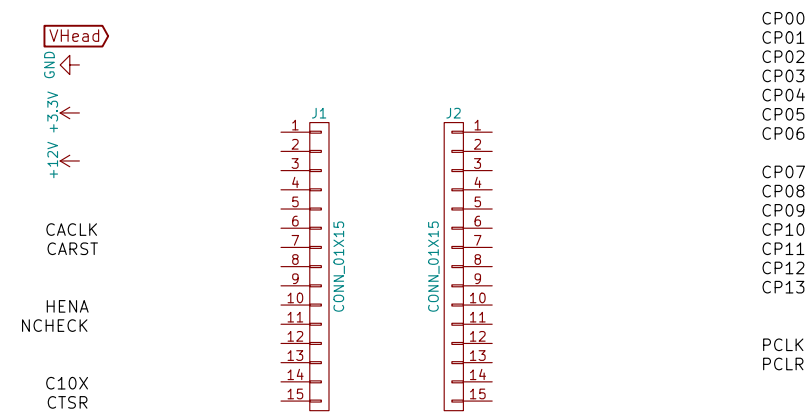
Nozzle check circuit



Sense resistors circuit



Board interface



CP00
CP01
CP02
CP03
CP04
CP05
CP06

CP07
CP08
CP09
CP10
CP11
CP12
CP13

PCLK
PCLR

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File: OA-HP45C3.sch

Title:

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